

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claim 1 (canceled)

2 (currently amended): A semiconductor integrated circuit device according to claim ~~1~~14, further comprising a power supply terminal to which an external power supply voltage is applied, wherein a transistor connected directly to the power supply terminal is one of the transistors other than the transistor having the thinnest gate insulation film.

3 (currently amended): A semiconductor integrated circuit device according to claim ~~1~~14, further comprising a power supply terminal to which an external power supply voltage is applied and a ground terminal, wherein a transistor having a current path connected between the power supply terminal and the ground terminal is one of the transistors other than the transistor having the thinnest gate insulation film.

Claim 4 (canceled)

5 (currently amended): A semiconductor integrated circuit device according to claim ~~4~~14, wherein a transistor included in the interface circuit and connected directly to a power supply terminal is one of the transistors other than the transistor having the thinnest gate insulation film.

en
7/19/05

6 (previously presented): A semiconductor integrated circuit device according to claim 5, wherein a transistor included in the interface circuit and having a current path connected between the power supply terminal and a ground terminal is one of the transistors other than the transistor having the thinnest gate insulation film.

Claims 7-13 (canceled)